INITIAL INFORMATION DATA SHEET

APPLICATION INFORMATION

Application Type::   Regular

Subject Matter::   Utility

Title::   Method and System for Inserting
                           Probe Points in FPGA-Based System-
                           On-Chip (SoC)

Docket Number::   X-998 US

Request for Early Pub?::   No

Request for Non-Pub?::   Yes

Total Drawing Sheets::   Five

Small Entity?::   No

Petition included?::   No

INVENTOR INFORMATION

Inventor Authority Type::   Inventor

Primary Citizenship Ctry:: US

Status::   Full Capacity

Given Name::   Reno L.

Family Name::   Sanchez

Street::   8520 Beverly Hills

City::   Albuquerque

State or Province::   New Mexico

Postal or Zip Code::   87122

Inventor Authority Type::   Inventor

Primary Citizenship Ctry:: US

Status::   Full Capacity

Given Name::   Douglas E.

Family Name::   Thorpe

Street::   13912 Encantado Road NE

City::   Albuquerque

State or Province::   New Mexico

Postal or Zip Code::   87123

Initial   02-22-02

CORRESPONDENCE INFORMATION

Correspondence Customer Number::   24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	

ASSIGNEE INFORMATION

Assignee Name::   Xilinx, Inc.

Street::   2100 Logic Drive

City::   San Jose

State or Province::   California

Postal or Zip Code::   95124